

JIS College of Engineering

Department of Electronics & Communication Engineering

Course Name: Digital Electronics Lab

CourseCode:EC391 Contact:0:0:3 Credit:1.5

List of Experiments

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1. Realization of basic gates and universal logic gates .
2. Realization of basic gates using Universal logic gates.
3. Design the circuit of Grey to Binary and vice versa.
4. Design a circuit for BCD to 7-segment display.
5. Design of Half Adder & Full Adder Circuit using Logic Gates.
6. Design Half Subtractor & Full Subtractor Circuit using Logic Gates.
7. Four-bit parity generator and Two-bit comparator circuits.
8. Construction of simple Encoder & Decoder circuits using logic gates.
9. Construction of simple Multiplexer & De Multiplexer circuits using logic gates.
10. Realization of RS,D, JK and T flip-flops using logic gates.
11. Realization of SISO/SIPO Register using flip-flops and logic gates.
12. Realization of synchronous Up/Down counters.
12. Realization of logic gates using TTL.
13. One Innovative design of Digital Circuits.